



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/715,694	11/17/2000	Ching-Chang Shen	TI-31777	8554

7590 04/15/2003

Dennis Moore
Texas Instruments Incorporated
Post Office Box 655474 M S 3999
Dallas, TX 75265

EXAMINER

RODRIGUEZ, ISABEL

ART UNIT PAPER NUMBER

2836

DATE MAILED: 04/15/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/715,694

Applicant(s)

SHEN, CHING-CHANG

Examiner

Isabel Rodriguez

Art Unit

2836

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 January 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-13 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-6, 9, and 11-13 are rejected under 35 U.S.C. 102(b) as being anticipated by Szepeso (US 5,018,041).

a) Regarding to claims 1, 3-5, 11 and 13, Szepesi discloses an overcurrent protection circuit for a motor drive (Fig. 5) comprising: a first FET (213) and a parallel second FET (202), having a gate input coupled to said first FET input gate and conducting a selectively programmable variable bias current (I'_{CL} see col. 6 lines 58-60) and a comparator (218), whose non-inverting input is directly coupled to the drain of said first FET and whose inverting input is directly coupled to the drain of said second FET, that generates an output indicative of motor current exceeding a predetermined threshold. As seen in figure 5, the transistors and the comparator have diodes and a lowpass filters between them. These devices enhance the output signal of the transistors but do not alter the output. The signals serving as inputs to the comparator are the output of the transistors. The examiner understands this to establish a direct connection between the comparator and the output or gate of the transistors.

b) Regarding claim 2, it is inherent that the overcurrent protection circuit wherein each FET gates are driven had by a voltage to generate a low on resistance between the respective source and drain.

c) Regarding claim 6, Szepesi discloses that the FET drive voltage is generated by a voltage pump(209).

d) Regarding claim 9, it is inherent that a tracking current source is selectively programmable.

e) Regarding claim 12, Szepesi discloses that the compactor has delay circuitry filtering out any transient current spikes through said first FET. See col. 8 lines 3-5.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 7-8 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable by Szepesi

a) Regarding claim 7, Szepesi discloses an overcurrent protection circuit with a voltage pump (level-shift) but gives no details on the amount by which the voltage is shifted. It would have been obvious to one having ordinary skill in the art at the time the invention was made to substitute the input gain to any desired value as long as it compatible with the requirements of other elements in the circuit in order to properly perform the driving function of the driving

Art Unit: 2836

signal. It has been held that discovering an optimum value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

b) Regarding claim 8, Szepesi discloses an over current protection circuit but does not specify that the ratio of said motor current to said bias current is proportional to a size of said first FET with respect to a size of said second FET. It would have been obvious to one having ordinary skill in the art at the time the invention was made to substitute the first and second FET size to any desired value as long as it compatible with the requirements of other elements in the circuit in order to properly performs the switching function of the circuit breaker switch. It has been held that discovering an optimum value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

c) Regarding claim 10, Szepesi discloses selectively programmable variable bias current and does not specify if controller is analog or digital. It would have been obvious to one of ordinary skill in the art at the time the invention was made to use a digital controller, thus making the threshold digitally programmable, because of the advantage of doing of performing faster calculations and many other benefits that are well known in the art. Regarding the digital controller, in absence of persuasive evidence that a particular type of control is significant, it would have been an obvious matter of choice to one of ordinary skill in the art to utilize any type of controller in order to control as long as it provide a the intended function of providing a control signal. *In re Dailey*, 357 F.2d 669, 149 USPQ 47 (CCPA 1966).

Response to Arguments

5. Applicant's arguments with respect to claims 1-13 have been considered but are moot in view of the new ground(s) of rejection. The amendments to the claims filed on 1/3/03 have

Art Unit: 2836

overcome the prior art. New grounds of rejection have been applied necessitated by the amendment. Please see above for rejection.

Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Isabel Rodriguez whose telephone number is 703-305-4761. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 703-308-3119. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7724 for regular communications and 703-308-7704 for After Final communications.

Application/Control Number: 09/715,694

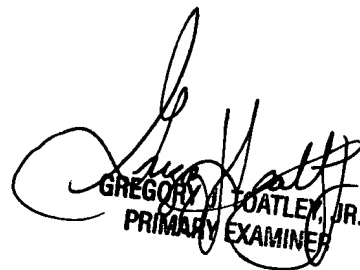
Page 6

Art Unit: 2836

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

IR

April 10, 2003


GREGORY A. TOATLEY, JR.
PRIMARY EXAMINER
14 Apr 03